E78-915TBL-02 User Manual

Test Kit

Chengdu Ebyte Electronic Technology Co.,Ltd.

Contents

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The test data obtained in the article are all obtained by the Ebyte laboratory, and the actual results may vary slightly.

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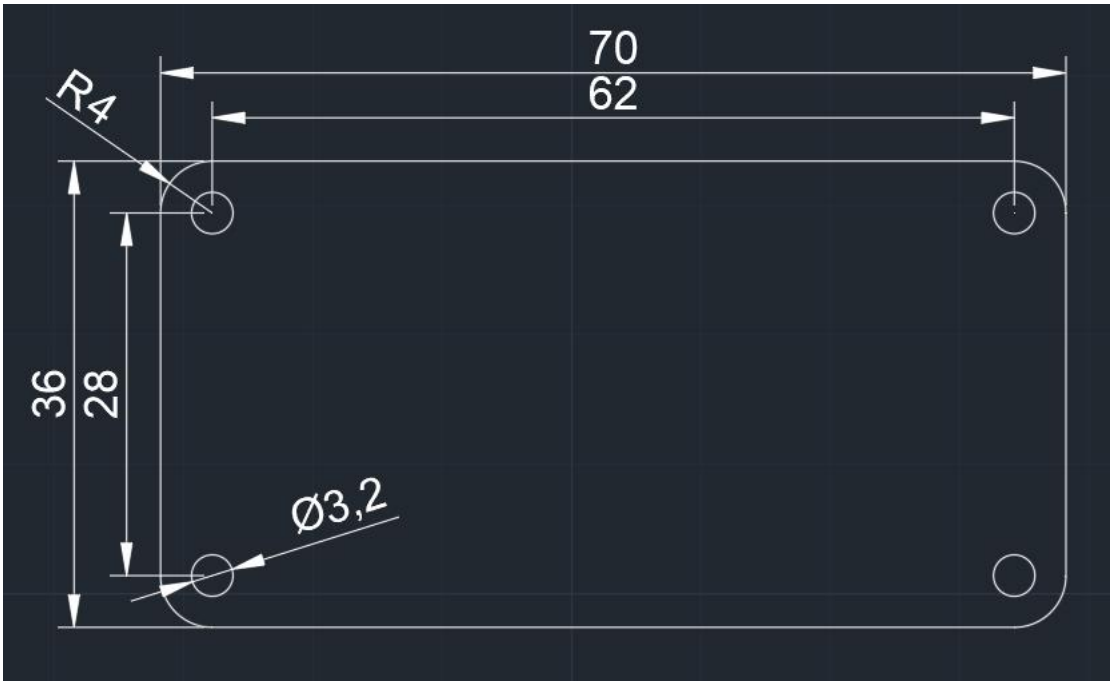
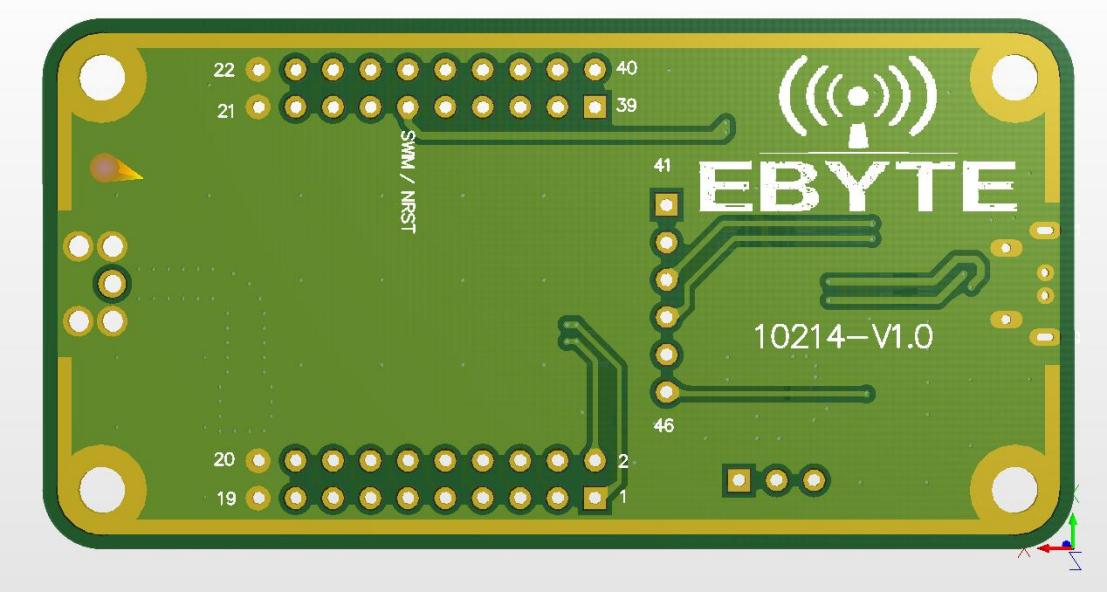
1. Introduction

E78-915TBL-02 is a complete set of test products specially for E78 series SMD SOC modules combined with USB to TTL serial port backplane. All E78-868LN22S (6601) module pins have been drawn out for customer testing and development, which greatly reduces customer testing and development difficulty.

1.1 Size and interface specification





Pin number	Definition	Function Description
1	LCD_SEG8	User-defined IO pin
2	LCD_SEG9	User-defined IO pin
3	LCD_SEG11	User-defined IO pin
4	LCD_SEG10	User-defined IO pin
5	LCD_SEG13	User-defined IO pin
6	LCD_SEG12	User-defined IO pin
7	LCD_SEG15	User-defined IO pin
8	LCD_SEG14	User-defined IO pin
9	LCD_SEG17	User-defined IO pin
10	LCD_SEG16	User-defined IO pin
11	I2C_SCL	Module I2C_SCL pin
12	I2C_SDA	Module I2C_SDA pin
13	ADC_IN1	Module ADC_IN1 input pin
14	ADC_IN0	Module ADC_IN0 input pin
15	GPIO3	User-defined IO pin
16	GPIO2	User-defined IO pin
17	ADC_IN2	Module ADC_IN2 input pin
18	GPIO4	User-defined IO pin
19	GND	Baseboard reference ground
20	GND	Baseboard reference ground
21	GND	Baseboard reference ground
22	GND	Baseboard reference ground
23	SPI_SLCK	Module SPI_SLCK pin
24	SPI_NSS	Module SPI_NSS pin
25	SPI_MOSI	Module SPI_MOSI pin
26	SPI_MISO	Module SPI_MISO pin
27	LCD_SEG2	User-defined IO pin

28	LCD_SEG1	User-defined IO pin
29	NRST	Module external reset pin
30	SWIM	Module SWIM pin
31	LCD_COM1	User-defined IO pin
32	LCD_COM0	User-defined IO pin
33	VREFP	Module ADC reference voltage input pin
34	LCD_COM2	User-defined IO pins
35	UART1_TX	Module UART1_TX pin
36	UART1_RX	Module UART1_RX pin
37	LCD_SEG0	User-defined IO pins
38	VLCD	Module VLCD pin, when it is LCD_xx, the pin should be connected to the power supply 3.3V
39	LCD_SEG3	User-defined IO pins
40	LCD_COM3	User-defined IO pins
41	LCD_SEG4	User-defined IO pins
42	LCD_SEG5	User-defined IO pins
43	UART0_RX	Module UART0_RX pin
44	UART0_TX	Module UART0_TX pin
45	LCD_SEG6	User-defined IO pins
46	LCD_SEG7	User-defined IO pins

2. Quick start

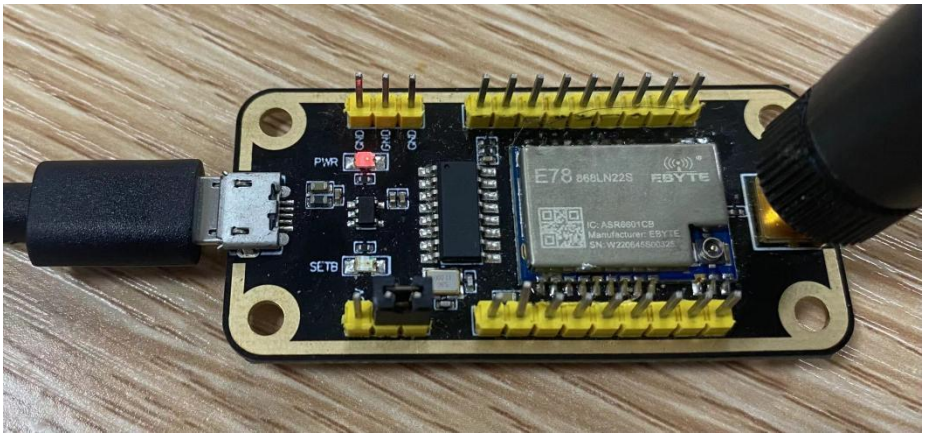
2.1 Test preparation

2.1.1 Driver Installation

Please go to the official website to download the driver CH341SER.EXE, and double-click to install. This driver supports 32/64-bit Windows 10/8.1/8/7/VISTA/XP, SERVER2016/2012/2008/2003, 2000/ME/98, certified by Microsoft digital signature, and supports USB to 3-wire and 9-wire serial ports etc.

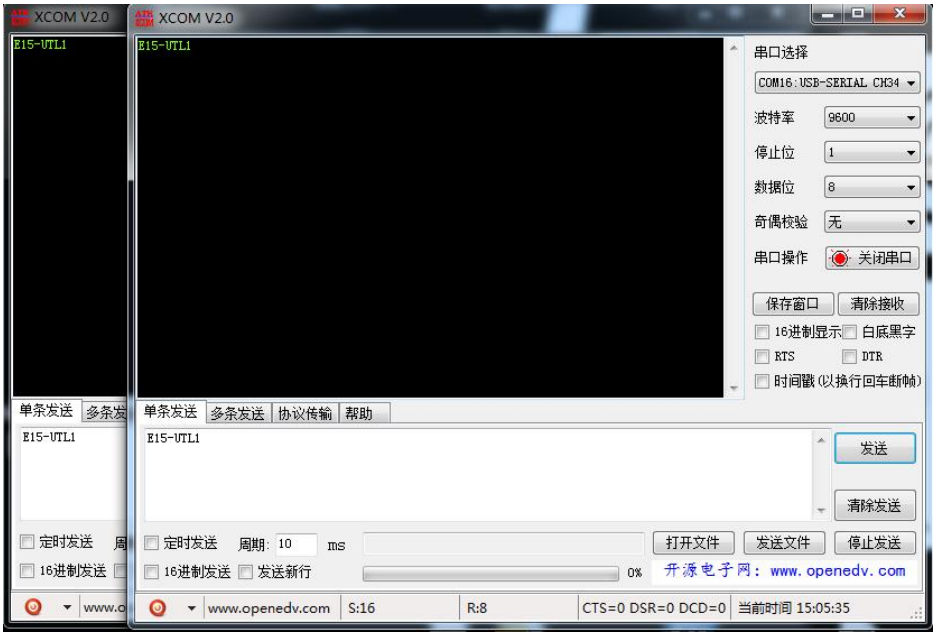
2.1.2 hardware connection

Please prepare the Micro USB cable and antenna, connect them to the E78-915TBL-02 accordingly, and open the corresponding serial port.



As shown in the figure, plug in the jumper cap and select 3.3V power supply. Both E78-915TBL-02 are configured in this way. Open the corresponding serial port to send and receive data.

（Please refer to the E78-915LN22S (6601) product manual for related instructions）



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